

Fig. 1

Conventional OCR:

Bits	Voltage Window	High Voltage MMC	Dual Voltage MMC
[6:0]	Reserved	0000000b	0000000b
[7]	1.65-1.95V	0b	1b
[14:8]	2.0V-2.6V	0000000b	0000000b
[23:15]	2.7V-3.6V	11111111b	11111111b
[30:24]	Reserved	0000000b	0000000b
[31]	Busy	0b Busy 1b Ready	0b Busy 1b Ready

Fig. 2

Proposed OCR:

Bits	Voltage Window	High Voltage MMC	Dual Voltage MMC
[6:0]	Reserved	0000000b	0000000b
[7]	1.65-1.95V	0b	1b
[14:8]	2.0V-2.6V	0000000b	0000000b
[23:15]	2.7V-3.6V	11111111b	11111111b
[30:24]	Timeout Value	xxxxxxxb	xxxxxxxb
[31]	Busy	0b Busy 1b Ready	0b Busy 1b Ready

Fig. 3

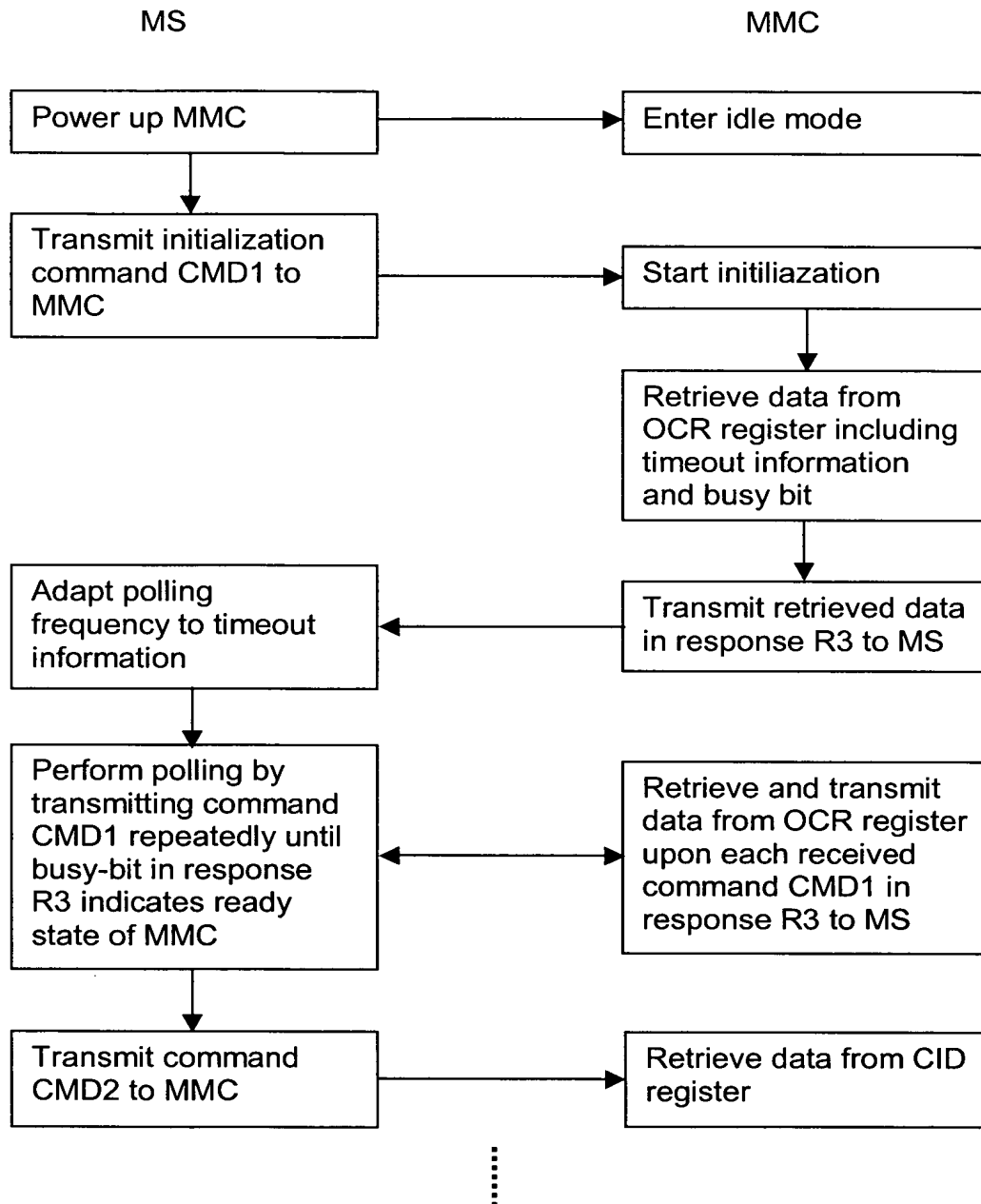


Fig. 4

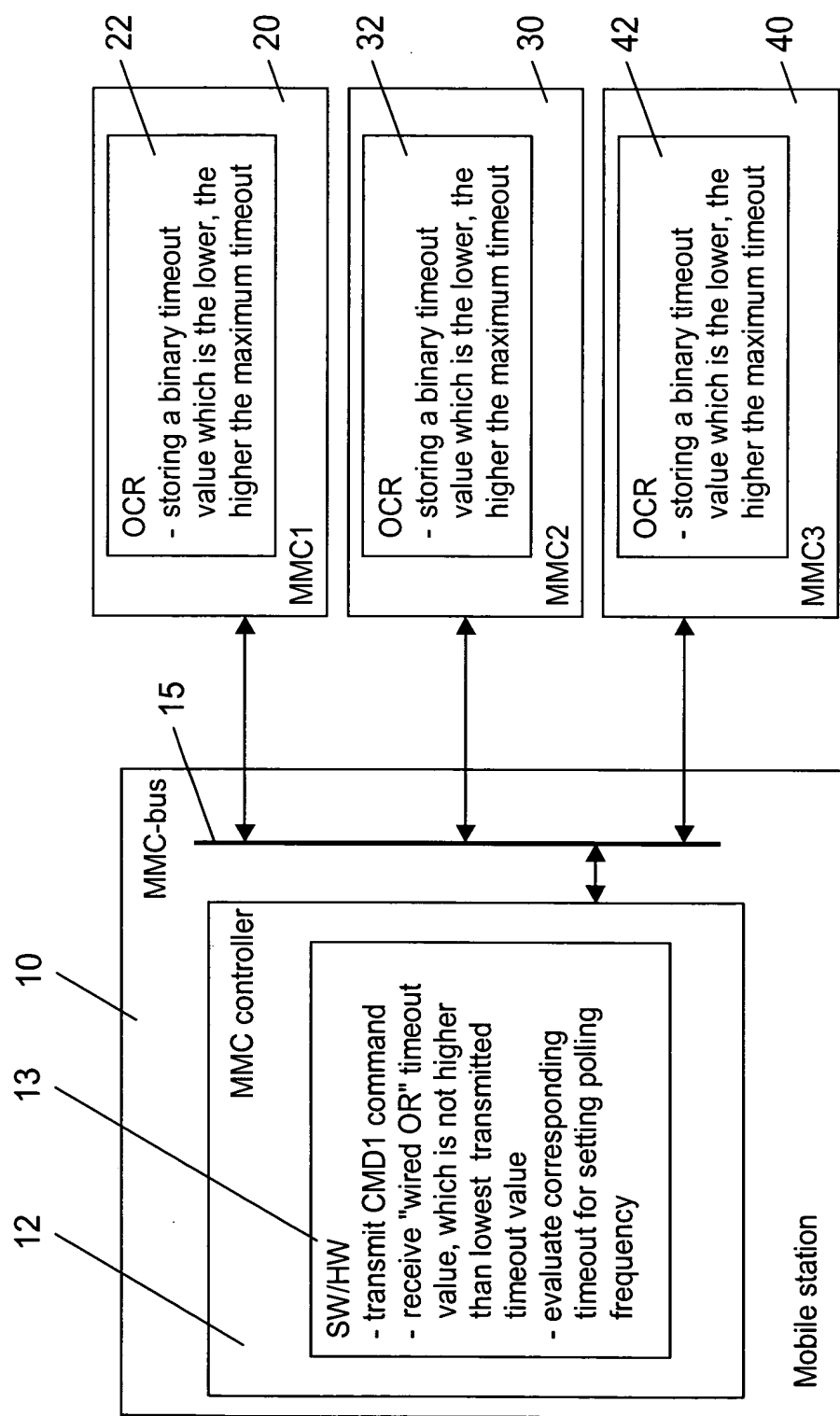


Fig. 5